## **EUROPEAN PATENT OFFICE**

## **Patent Abstracts of Japan**

**PUBLICATION NUMBER** 

11074631

**PUBLICATION DATE** 

16-03-99

APPLICATION DATE

27-08-97

APPLICATION NUMBER

09230718

APPLICANT: MITSUBISHI ELECTRIC CORP;

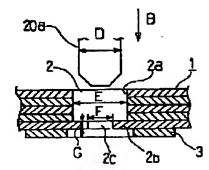
INVENTOR: SATO HIROSHI;

INT.CL.

H05K 1/18

TITLE

**ELECTRONIC CIRCUIT BOARD** 



ABSTRACT:

PROBLEM TO BE SOLVED: To prevent defective mounting due to I:ft-off, etc., of electronic parts by preventing parting of terminal of electronic parts.

SOLUTION: Relating to an electronic circuit board wherein electronic parts are mounted by connecting a terminal 20a of an electronic part penetrating a terminal insert part 2 from one surface of a substrate and then protruding above the other surface to a land part 3 provided on the side of the other surface by soldering, a terminal holding means 2a is provided wherein the terminal insert part 2, formed thinner than the substrate, is elastically deformed by the terminal 20a when it is inserted, whose restoring force holds the terminal 20a.

COPYRIGHT: (C) 1999, JPO